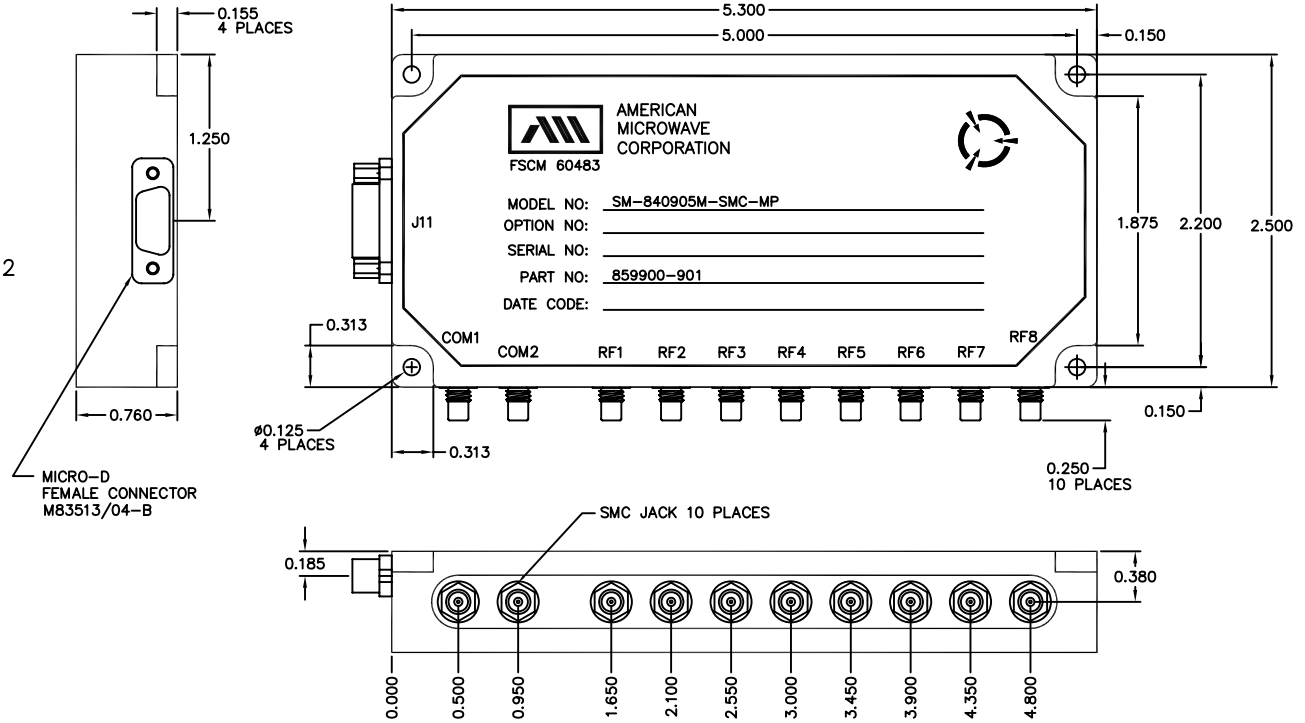


REVISIONS				
ZONE	REV.	DESCRIPTION	DATE	APPROVED
	A	SEE ER# 068-10	8/31/10	Rd

SPECIFICATIONS

- FREQUENCY RANGE: 840 – 905MHz
- INSERTION LOSS: 2.5dB MAXIMUM
- ISOLATION: 60dB MINIMUM
- VSWR (INPUT/OUTPUT): 1.5:1 MAXIMUM
- VSWR (TERMINATION): 2.0:1 MAXIMUM
- POWER HANDLING: 3 WATT CW INJECTED
AT THE COMMON PORT
- SWITCHING SPEED: 300nSEC MAXIMUM
- CONTROL: $\leq 0.5V$ = LOW = FALSE
..... $\geq 2.0V$ = HIGH = TRUE
SEE LOGIC TABLE ON SHEET 2
- POWER SUPPLY: +15V @ 350 mA MAXIMUM



ENVIRONMENTAL RATINGS:

- TEMPERATURE: -40°C TO +85°C (OPERATING)
..... -65°C TO +125°C (STORAGE)
- HUMIDITY: MIL-STD-202F, METHOD 103B COND. B
- SHOCK: MIL-STD-202F, METHOD 213B COND. B
- VIBRATION: MIL-STD-202F, METHOD 204D COND. B
- ALTITUDE: MIL-STD-202F, METHOD 105C COND. B
- TEMPERATURE CYCLE: MIL-STD-202F, METHOD 107D COND. A

NOTE: SPECIFICATIONS WILL VARY OVER OPERATING TEMPERATURE
NOTE: THE ABOVE SPECIFICATIONS ARE SUBJECT TO CHANGE OR REVISION

* Units are designed to meet Environmental ratings but not tested. If Environmental Testing is required, please contact Sales Department.

ALL DIMENSIONS ARE IN INCHES
TOLERANCES:
X.XX ±0.020
X.XXX ±0.010

AMERICAN MICROWAVE CORPORATION

7311-G GROVE ROAD
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ISO 9001:2000 CERTIFIED



APPROVALS		DATE	TITLE			REV.
DRAWN <i>K JPMMS</i>		8/31/10	PRODUCT FEATURE SM-840905M-SMC-MP TECOM 859900-901			A
ENG. <i>Rd</i>		8/31/10	SIZE A	FSCM NO. 60483	DWG NO. 100-7985	
QAE <i>K JPMMS</i>		8/31/10	SCALE N:S			SHEET 1 OF 2

REVISIONS				
ZONE	REV.	DESCRIPTION	DATE	APPROVED
	A	SEE ER# 068-10	8/31/10	RA

SWITCH CONTROL								COM1	COM2
SP8T-A				SP8T-B				THRU	THRU
CO	C1	C2	C3	C4	C5	C6	C7	TO	TO
0	0	0	0	X	X	X	X	RF1	X
1	0	0	0	X	X	X	X	RF2	X
0	1	0	0	X	X	X	X	RF3	X
1	1	0	0	X	X	X	X	RF4	X
0	0	1	0	X	X	X	X	RF5	X
1	0	1	0	X	X	X	X	RF6	X
0	1	1	0	X	X	X	X	RF7	X
1	1	1	0	X	X	X	X	RF8	X
0	0	0	1	X	X	X	X	ALL OFF	X
X	X	X	X	0	0	0	0	X	RF1
X	X	X	X	1	0	0	0	X	RF2
X	X	X	X	0	1	0	0	X	RF3
X	X	X	X	1	1	0	0	X	RF4
X	X	X	X	0	0	1	0	X	RF5
X	X	X	X	1	0	1	0	X	RF6
X	X	X	X	0	1	1	0	X	RF7
X	X	X	X	1	1	1	0	X	RF8
X	X	X	X	0	0	0	1	X	ALL OFF
X	X	X	1	X	X	X	1	A AND B	ALL OFF
1	1	1	1	1	1	1	1	STATUS	RESET

PIN OUT FOR J11	
PIN	FUNCTION
1	CO
2	C1
3	C2
4	C3
5	STATUS
6	N/C
7	N/C
8	GND
9	C4
10	C5
11	C6
12	C7
13	GND
14	+15V
15	+15V

NOTES:

1. C3, C2, C1 & CO MUST NOT EQUAL C7, C6, C5 & C4 EXCEPT FOR THE A AND B ALL OFF STATE. ALL CONTROL INPUTS ARE 3.3V TTL.
2. THE MTSM SHALL HAVE A BUILT IN TEST CAPABILITY. THE BUILT IN TEST SHALL DETECT A FAILURE IN ANY OF THE SELECTABLE RF PATHS. THE STATUS BIT CIRCUIT SHALL PROVIDE LOW VOLTAGE TTL LOW OUTPUT (LVTTL"0"=NORMAL) WHEN THE SWITCH IS OPERATING NORMALLY AND A LOW VOLTAGE TTL HIGH OUTPUT (TTL "1"=FAIL) WHEN ANY ONE OF THE SWITCH PATHS ARE INOPERATIVE. ONLY A STATUS RESET SIGNAL INPUT OR A +15V RESTART SHALL CAUSE A RESET OF THE STATUS OUTPUT TO LOW VOLTAGE TTL="0" NORMAL.

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APPROVALS		DATE	TITLE			
DRAWN	<i>K JPMMP</i>	8/31/10	PRODUCT FEATURE			
ENG.	<i>RA</i>	8/31/10	SM-840905M-SMC-MP			
QA	<i>K JPMMP</i>	8/31/10	TECOM 859900-901			
SIZE	FSCM NO.	DWG NO.	REV.			
A	60483	100-7985	A			
SCALE	N:S		SHEET		2 OF 2	